

DEC 04 2007

N. Kenneth Burraston

**KIRTON &  
McCONKIE**TELEPHONE (801) 328-3600  
FACSIMILE (801) 321-4893A PROFESSIONAL CORPORATION  
1800 EAGLE GATE TOWER  
60 EAST SOUTH TEMPLE  
P.O. BOX 45120  
SALT LAKE CITY, UTAH 84145-0120**FACSIMILE TRANSMISSION SHEET**

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3 NUMBER OF PAGES INCLUDING COVER SHEET

COMMENTS: Transmitted herewith is a Terminal Disclaimer and Cover Page for:

App. No.: 10/034,528

Confirmation No.: 2563

Applicant: Eldridge et al.

Filed: December 27, 2001

TC/A.U.: 2829

Examiner: Paresh H. Patel

Docket No.: P6C3-US

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/Erin Cowles/  
Erin Cowles

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Appl. No. :	10/034,528	Confirmation No. 2563
Applicant :	Eldridge et al.	
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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESUBMISSION OF TERMINAL DISCLAIMER**

To Whom It May Concern:

In response to a telephone request from the Examiner, Applicants resubmit the attached Terminal Disclaimer modified to include the signing attorney's registration number. Applicants believe that the fee for the terminal disclaimer has already been paid, but Applicants authorize the USPTO to charge any fee due, including a terminal disclaimer fee, to deposit account no. 50-0843.

Respectfully submitted,

Date: December 4, 2007

By /N. Kenneth Burraston/  
N. Kenneth Burraston  
Reg. No. 39,923

Kirton & McConkie  
P.O. Box 45120  
Salt Lake City, Utah 84145-0120  
Telephone: (801) 426-2106  
Fax: (801) 321-4893

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PTO/SB/26 (06-03)

Approved for use through 07/31/2008. OMB 0651-0031

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## REJECTION OVER A PRIOR PATENT

Docket Number (Optional)

P6C3-US

In re Application of: Eldridge et al

Application No.: 10/034,528

Filed: December 27, 2001

For: A tested semiconductor device produced by adjusting a planar orientation of probe elements of a probe card

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- 2 ☒ The undersigned is an attorney or agent of record

/N. Kenneth Burraston/

December 4, 2007

Signature

Date

N. Kenneth Burraston

Reg. No. 39,923

Typed or printed name

(801) 426-2106

Telephone Number

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